## ABSTRACT OF THE DISCLOSURE

A system and method for measuring the response time of a differential signal pair squelch detection circuit is provided. First, both the positive and negative signal lines of the differential signal pair are driven with square waves of different frequencies, typically by way of a signal generator. Thereafter, either the phase or the frequency of the two square waves is gradually altered until the duty cycle of a squelch detect signal of the squelch detection circuit falls below fifty percent. The resulting period of the square waves, or the relative phase between the two, is then utilized to calculate the response time of the squelch detection circuit.

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